

Amendments to the Specification:

Please replace the current title of the application with the following new title:

**METHOD AND APPARATUS FOR PROVIDING PRE AND POST HANDLERS FOR
RECORDING EVENTS METHOD AND SYSTEM FOR RECORDING EVENTS OF AN
INTERRUPT USING PRE AND POST HANDLERS**

Please replace the paragraph extending from page 1, line 4 to page 2, line 21 with the following new paragraph:

The present invention is related to the following applications entitled "Method and Apparatus for Counting Instruction Execution and Data Accesses", serial no. 10/675,777, attorney docket no. AUS920030477US1, filed on September 30, 2003; "Method and Apparatus for Selectively Counting Instructions and Data Accesses", serial no. 10/674,604, attorney docket no. AUS920030478US1, filed on September 30, 2003; "Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations", serial no. 10/675,831, attorney docket no. AUS920030479US1, filed on September 30, 2003; "Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold", serial no. 10/675,778, attorney docket no. AUS920030480US1, filed on September 30, 2003; "Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations", serial no. 10/675,776, attorney docket no. AUS920030481US1, filed on September 30, 2003; "Method and Apparatus for Debug Support for Individual Instructions and Memory Locations", serial no. 10/675,751, attorney docket no. AUS920030482US1, filed on September 30, 2003; "Method and Apparatus to Autonomically Select Instructions for Selective Counting", serial no. 10/675,721, attorney docket no. AUS920030483US1, filed on September 30, 2003; "Method and Apparatus to Autonomically Count Instruction Execution for Applications", serial no. 10/674,642, attorney docket no. AUS920030484US1, filed on September 30, 2003; "Method and Apparatus to Autonomically Take an Exception on Specified Instructions", serial no. 10/674,606, attorney docket no. AUS920030485US1, filed on September 30, 2003; "Method and Apparatus to Autonomically Profile Applications", serial no. 10/675,783, attorney docket no. AUS920030486US1, filed on September 30, 2003; "Method and Apparatus for Counting Instruction and Memory Location Ranges", serial no. 10/675,872, attorney docket no. AUS920030487US1, filed on September 30, 2003; "Method and Apparatus for Qualifying Collection of Performance Monitoring Events by Types of Interrupt When Interrupt Occurs", serial no. 10/757,186, attorney docket no.

AUS920030541US1, filed on January 14, 2004; and "Method and Apparatus for Counting Interrupts by Type", serial no. 10/757,198, attorney docket no. AUS920030541US1, filed on January 14, 2004. All of the above related applications are assigned to the same assignee, and incorporated herein by reference.

Please replace the paragraph extending from page 31, line 11 to page 31, line 26 with the following new paragraph:

Alternatively, this information may be maintained in a data structure that is accessed by the processor or the interrupt unit each time there is a trap or interrupt generated to determine whether that particular trap or interrupt is associated with an enabled pre handler, post handler, both or neither. This data structure may be, for example, the IDT, a shadow IDT data structure, or other data structure. In another embodiment, a register may be used to indicate the address of a pre handler and a different register for a post handler. The value of zero means there is no pre or post handler and a non-zero value indicates the address of the pre or post handler. This approach allows one pre handle and one post handler for all traps. ~~[Please note that is probably the preferred embodiment and should definitely be in the claims.]~~